**CSE 206 (Digital Logic Design Sessional)**

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| 02 |

**Expt. No.**

**Name of the Expt.**

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| Truth tables and simplification using Boolean Algebra |

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| **Group No.** | **06** |
| **Writer’s Roll** | **1705027** |
| **Section** | A |
| **Department** | CSE |
| **Other Group Members** | 1705026  1705028  1705029  1705030 |
| **Date of Performance:** | 07-05-2019 |
| **Date of Submission:** | 14-05-2019 |